

QUAID-E-AWAM UNIVERSITY OF ENGINEERING, SCIENCE & TECHNOLOGY, NAWABSHAH.
 The Quaid-e-Awam University of Engineering, Science & Technology announces the result of
Regular Examination of First Term – Second Year (3rd Term) Bachelor of Engineering (Electronic)
 L-11-Batch held in May / June – 2012 at **Quaid-e-Awam University College of Engineering,
 Science & Technology, Larkano** as under:

BACHELOR OF ENGINEERING (ELECTRONIC)

MAXIMUM MARKS: 650

Candidates having the following Identification Numbers (I.D. Nos) are declared successful.

ID No. L-11ES06(404), 07(411-APR), 11(364-APR), 13(422), 15(526), 25(547), 40(472), 41(446),
 42(447), 46(344-APR), 48(476), 50(408), 52(325), 53(435), 54(448), 56(402), 57(417),
 59(477), 60(362-APR), 66(364), 68(421), 72(351-APR), 73(405)

LIST OF FAILURES

I.D No.	SUBJECTS
L-11ES05	Circuit Analysis (Th)
L-11ES08	Failing in all subjects
L-11ES23	Differential Equations & Fourier Series, Circuit Analysis (Th), Digital Logic Design (Th&Pr)
L-11ES35	Differential Equations & Fourier Series, Amplifiers & Oscillators (Th&Pr), Circuit Analysis (Th), Digital Logic Design (Th), Engineering Economics & Management
L-11ES36	Differential Equations & Fourier Series, Circuit Analysis (Th)
L-11ES38	Failing in all subjects except Engineering Economics & Management
L-11ES44	Amplifiers & Oscillators (Th)
L-11ES45	Failing in all subjects
L-11ES49	Differential Equations & Fourier Series, Amplifiers & Oscillators (Th), Circuit Analysis (Th&Pr), Digital Logic Design (Pr), Engineering Economics & Management
L-11ES51	Amplifiers & Oscillators (Th), Circuit Analysis (Th&Pr), Digital Logic Design (Th)
L-11ES61	Differential Equations & Fourier Series, Amplifiers & Oscillators (Th&Pr), Circuit Analysis (Th), Digital Logic Design (Th)
L-11ES63	Differential Equations & Fourier Series, Circuit Analysis (Th), Digital Logic Design (Th)
L-11ES64	Circuit Analysis (Pr)
L-11ES67	Failing in all subjects except Digital Logic Design (Pr)
L-11ES69	Circuit Analysis (Th)
L-11ES71	Amplifiers & Oscillators (Th), Circuit Analysis (Pr)
L-11-10ES19	Failing in all subjects except Engineering Economics & Management
L-11-10ES60	Differential Equations & Fourier Series, Circuit Analysis (Th)
L-11-10ES64	Differential Equations & Fourier Series, Amplifiers & Oscillators (Th), Circuit Analysis (Th&Pr), Digital Logic Design (Th&Pr)

NOTE: The University reserves the right to correct any error, mistake or omission in the results or certificates at any time upon verification of the record.

Nawabshah Dated **17-12-2012**


 CONTROLLER OF EXAMINATIONS