

QUAID-E-AWAM UNIVERSITY OF ENGINEERING, SCIENCE & TECHNOLOGY, NAWABSHAH.

The Quaid-e-Awam University of Engineering, Science & Technology announces the result of **Regular Examination of First Term – Third Year (5th Term) Bachelor of Engineering (Electronic) L-10-Batch** held in May / June – 2012 at **Quaid-e-Awam University College of Engineering, Science & Technology, Larkano** as under:

BACHELOR OF ENGINEERING (ELECTRONIC)

MAXIMUM MARKS: 700

Candidates having the following Identification Numbers (I.D. Nos) are declared successful.


ID No. L-10ES02(456), 10(445), 12(408), 20(404-APR), 24(404-APR), 26(470), 29(400), 34(489), 36(426), 39(491), 40(410), 42(422), 43(377-APR), 45(517), 49(367-APR), 53(404), 59(497), 63(414-APR), 65(377), 66(483), 67(391), 68(393-APR), 70(418), 72(482)

LIST OF FAILURES

I.D No.	SUBJECTS
L-10ES03	Failing in all subjects except Signals & Systems (Th&Pr)
L-10ES05	Integrated Circuit Design & Applications (Th), Embedded System Design (Th), Probability & Random Variables, Signals & Systems (Th)
L-10ES07	Integrated Circuit Design & Applications (Th), Control Systems (Pr)
L-10ES08	Integrated Circuit Design & Applications (Th), Embedded System Design (Th), Signals & Systems (Th)
L-10ES09	Integrated Circuit Design & Applications (Th), Embedded System Design (Th)
L-10ES11	Integrated Circuit Design & Applications (Th), Embedded System Design (Th)
L-10ES18	Embedded System Design (Th), Signals & Systems (Th)
L-10ES25	Probability & Random Variables
L-10ES27	Integrated Circuit Design & Applications (Th&Pr), Embedded System Design (Th)
L-10ES33	Integrated Circuit Design & Applications (Th), Embedded System Design (Th)
L-10ES44	Integrated Circuit Design & Applications (Pr), Embedded System Design (Th)
L-10ES46	Failing in all subjects except Signals & Systems (Pr)
L-10ES50	Integrated Circuit Design & Applications (Th), Embedded System Design (Th)
L-10ES54	Integrated Circuit Design & Applications (Th)
L-10ES55	Embedded System Design (Th), Control Systems (Th)
L-10ES56	Integrated Circuit Design & Applications (Th), Embedded System Design (Th), Probability & Random Variables, Control Systems (Th)
L-10ES57	Integrated Circuit Design & Applications (Th&Pr), Embedded System Design (Th), Control Systems (Th)
L-10ES58	Embedded System Design (Th), Control Systems (Pr)
L-10ES62	Embedded System Design (Th)
L-10ES69	Embedded System Design (Th), Probability & Random Variables, Signals & Systems (Th), Control Systems (Th)
L-10ES71	Embedded System Design (Th)

NOTE: The University reserves the right to correct any error, mistake or omission in the results or certificates at any time upon verification of the record.

Nawabshah Dated **22-11-2012**


CONTROLLER OF EXAMINATIONS