

QUAID-E-AWAM UNIVERSITY OF ENGINEERING, SCIENCE & TECHNOLOGY, NAWABSHAH

The Quaid-e-Awam University of Engineering, Science & Technology announces the result of **Regular Examination of First Term – Second Year (3rd Term) Bachelor of Engineering (Electronic) 12-Batch** held in May / June - 2013 as under:

BACHELOR OF ENGINEERING (ELECTRONIC)
MAXIMUM MARKS: 650

Candidates having the following Identification Numbers (I.D. Nos.) are declared successful.


ID NO. 12ES02(573), 04(464), 07(482), 09(482), 10(461-APR), 18(425), 25(360), 29(377), 31(387), 34(565), 36(551), 38(575), 40(517), 45(397), 48(457), 50(508), 51(402), 72(436), 73(465), 75(533), 77(481), 79(428-APR), 81(433), 85(459), 86(365-APR), 87(570), 88(543), 90(440)

LIST OF FAILURES

I.D No.	SUBJECTS
12ES03	Differential Equations & Fourier Series, Circuit Analysis (Th)
12ES06	Differential Equations & Fourier Series
12ES08	Differential Equations & Fourier Series
12ES11	Circuit Analysis (Th)
12ES12	Differential Equations & Fourier Series, Circuit Analysis (Th)
12ES13	Differential Equations & Fourier Series, Circuit Analysis (Th)
12ES19	Circuit Analysis (Th)
12ES32	Engineering Economics & Management, Amplifiers & Oscillators (Th), Differential Equations & Fourier Series, Digital Logic Design (Th), Circuit Analysis (Th)
12ES35	Differential Equations & Fourier Series
12ES39	Differential Equations & Fourier Series, Digital Logic Design (Th), Circuit Analysis (Th)
12ES41	Circuit Analysis (Th)
12ES44	Differential Equations & Fourier Series
12ES54	Circuit Analysis (Th)
12ES56	Amplifiers & Oscillators (Th), Differential Equations & Fourier Series, Circuit Analysis (Th)
12ES57	Amplifiers & Oscillators (Th), Differential Equations & Fourier Series, Digital Logic Design (Th), Circuit Analysis (Th)
12ES60	Circuit Analysis (Th)
12ES62	Differential Equations & Fourier Series
12ES63	Differential Equations & Fourier Series, Digital Logic Design (Th)
12ES64	Differential Equations & Fourier Series
12ES66	Differential Equations & Fourier Series
12ES74	Amplifiers & Oscillators (Th), Digital Logic Design (Th), Circuit Analysis (Th)
12ES78	Differential Equations & Fourier Series, Digital Logic Design (Th)
12ES80	Amplifiers & Oscillators (Th), Differential Equations & Fourier Series, Digital Logic Design (Th), Circuit Analysis (Th)
12ES82	Differential Equations & Fourier Series, Digital Logic Design (Th), Circuit Analysis (Th)
12ES83	Circuit Analysis (Th&Pr)
12ES84	Engineering Economics & Management, Amplifiers & Oscillators (Th), Differential Equations & Fourier Series, Circuit Analysis (Th)
12ES89	Amplifiers & Oscillators (Th), Differential Equations & Fourier Series, Digital Logic Design (Th), Circuit Analysis (Th)
12-11ES29	Amplifiers & Oscillators (Th), Differential Equations & Fourier Series, Digital Logic Design (Th), Circuit Analysis (Th)

NOTE: The University reserves the right to correct any error, mistake or omission in the results or certificates at any time upon verification of the record.

Nawabshah Dated: **13.02.2014**


CONTROLLER OF EXAMINATIONS