

QUAID-E-AWAM UNIVERSITY OF ENGINEERING, SCIENCE & TECHNOLOGY, NAWABSHAH

The Quaid-e-Awam University of Engineering, Science & Technology announces the result of **Regular** Examination of **First Term – Third Year (5th Term) Bachelor of Engineering (Electronic) L-11-Batch** held in June-2013 at **Quaid-e-Awam University College of Engineering, Science & Technology, Larkano** as under:

BACHELOR OF ENGINEERING (ELECTRONIC)

MAXIMUM MARKS: 700

Candidates having the following Identification Numbers (I.D. Nos) are declared successful.


ID NO. L-11ES05(435), 06(488), 07(480), 11(508), 13(469-APR), 15(576), 25(619), 40(494), 41(482), 42(512), 46(442), 48(536), 49(456), 53(441-APR), 54(568), 57(490), 59(563), 60(467), 64(465), 68(447-APR)

LIST OF FAILURES

I.D No.	SUBJECTS
L-11ES08	Failing in all subjects
L-11ES23	Integrated Circuit Design & Applications (Th&Pr), Embedded System Design (Th), Signals & Systems (Th&Pr), Control Systems (Th&Pr)
L-11ES35	Integrated Circuit Design & Applications (Th), Embedded System Design (Th), Signals & Systems (Th), Probability & Random Variables, Control Systems (Th)
L-11ES36	Integrated Circuit Design & Applications (Th), Probability & Random Variables
L-11ES38	Failing in all subjects
L-11ES44	Integrated Circuit Design & Applications (Th)
L-11ES45	Integrated Circuit Design & Applications (Th&Pr), Embedded System Design (Th), Signals & Systems (Th), Probability & Random Variables
L-11ES50	Integrated Circuit Design & Applications (Th)
L-11ES51	Integrated Circuit Design & Applications (Th), Signals & Systems (Th), Probability & Random Variables
L-11ES52	Integrated Circuit Design & Applications (Th&Pr), Embedded System Design (Th), Probability & Random Variables
L-11ES56	Integrated Circuit Design & Applications (Th), Probability & Random Variables
L-11ES61	Failing in all subjects except Embedded System Design (Pr)
L-11ES63	Probability & Random Variables
L-11ES66	Integrated Circuit Design & Applications (Pr), Signals & Systems (Th), Probability & Random Variables
L-11ES67	Integrated Circuit Design & Applications (Th&Pr), Signals & Systems (Th), Probability & Random Variables
L-11ES69	Probability & Random Variables
L-11ES71	Integrated Circuit Design & Applications (Th), Embedded System Design (Th), Signals & Systems (Th), Probability & Random Variables
L-11ES72	Probability & Random Variables
L-11ES73	Embedded System Design (Th), Probability & Random Variables
L-11-10ES19	Failing in all subjects
L-11-10ES60	Integrated Circuit Design & Applications (Th), Embedded System Design (Th), Probability & Random Variables

NOTE: The University reserves the right to correct any error, mistake or omission in the results or certificates at any time upon verification of the record.

Nawabshah Dated **20-12-2013**


CONTROLLER OF EXAMINATIONS